



Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Application Number	10/716,595
Filing Date	November 20, 2003
First Named Inventor	Michael FARMWALD, et al.
Art Unit	2827
Examiner Name	Nguyen, Tan
Confirmation No.	7213
Attorney Docket Number	57941.000062

Sheet 1 of 23

**U.S. PATENT DOCUMENTS**

*Examiner Initials	Cite No.	DOCUMENT NUMBER Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	1.	US 4,438,494	03-20-1984	Budde, et.al	
	2.	US 5,179,687	01-12-1993	Hidaka	
	3.	US 5,268,865	12-07-1993	Takasugi	
	4.	US 5,329,489	07-12-1994	Diefendorff	
	5.	US 5,343,425	08-30-1994	Saito	
	6.	US 5,384,745	01-24-1995	Konishi	
	7.	US 5,404,338	04-04-1995	Murai	
	8.	US 5,539,696	07-23-1996	Patel	
	9.	US 5,581,746	12-03-1993	Watanabe	
	10.	US 5,585,745	12-17-1996	Simmons et. al	
	11.	US 5,790,447	08-04-1998	Laudon	
	12.	US 5,829,026	11-10-1998	Leung, et al.	
		US			

**FOREIGN PATENT DOCUMENTS**

*Examiner Initial	Cite No.	FOREIGN PATENT DOCUMENT		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	TRANSLATION	
		Country Code Number-Kind Code (if known)					YES	NO
	13.	EP	0 509 811	10-21-1992	Doska		<input checked="" type="checkbox"/>	<input type="checkbox"/>
	14.	EP	0 605 887	07-13-1994	Watanabe		<input checked="" type="checkbox"/>	<input type="checkbox"/>
	15.	EP	0 994 420	04-19-2000	Farmwald		<input checked="" type="checkbox"/>	<input type="checkbox"/>
	16.	EP	0-339-224	11-02-1989	Bowater, et. al.		<input checked="" type="checkbox"/>	<input type="checkbox"/>
	17.	EP	1 004 956	01-03-2001	Farmwald		<input checked="" type="checkbox"/>	<input type="checkbox"/>

EXAMINER SIGNATURE

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		Country Code	Number-Kind Code (if known)				YES	NO
	18.	EP	1 004 956 A2	05-31-2000	Farmwald		<input checked="" type="checkbox"/>	<input type="checkbox"/>
	19.	EP	1 004 956 B1	01-03-2001	Farmwald		<input checked="" type="checkbox"/>	<input type="checkbox"/>
	20.	EP	1 022 641	07-26-2000	Farmwald		<input checked="" type="checkbox"/>	<input type="checkbox"/>
	21.	EP	1 022 642 A1	07-26-2000	Farmwald		<input checked="" type="checkbox"/>	<input type="checkbox"/>
	22.	EP	1 022 642 B1	09-05-2001	Farmwald		<input checked="" type="checkbox"/>	<input type="checkbox"/>
	23.	EP	1 197 830	04-19-2002	Farmwald		<input checked="" type="checkbox"/>	<input type="checkbox"/>
	24.	DE	37 33 554	04-21-1988	Lofgren		<input type="checkbox"/>	<input checked="" type="checkbox"/>

OTHER DOCUMENTS - NON-PATENT LITERATURE DOCUMENTS				
*Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	TRANSLATION	
			YES	NO
	25.	Blake, Jane (Editor), "CVAX-Based Systems," Digital Technical Journal - Digital Equipment Corporation, No. 7 pp. 1-143, August 1988.	<input type="checkbox"/>	<input type="checkbox"/>
	26.	Bursky, "Fast DRAMs Can Be Swapped for SRAM Caches," Electronic Design Report, July 22, 1993	<input type="checkbox"/>	<input type="checkbox"/>
	27.	Bursky, "Triple-Port Dynamic RAM Accelerates Data Movement," Electronic Design, A Penton Publication, May 24, 1990.	<input type="checkbox"/>	<input type="checkbox"/>
	28.	Chipworks, "MoSys MD909SJ-5-100 288Kx32Bit Multibank DRAM Circuit Analysis Report," (Revised 1.0 - October 19, 2007; Rev. 2.0 - January 16, 2008 and Rev. 3.0 - April 22, 2008)	<input type="checkbox"/>	<input type="checkbox"/>
	29.	Chipworks, "MoSys MD909SJ-5-100 288Kx32Bit Multibank DRAM Identification Report," published April 11, 2007.	<input type="checkbox"/>	<input type="checkbox"/>

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<b>OTHER DOCUMENTS - NON-PATENT LITERATURE DOCUMENTS</b>				
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			YES	NO
	30.	Claim Chart Comparison for Claim 1 of EP 0 994 420 in the European Opposition Proceedings for EP 1 197 830 dated May 21, 2008.	<input checked="" type="checkbox"/>	<input type="checkbox"/>
	31.	Claim Construction Order For The Farmwald/Horowitz Patents And Order Denying The Manufacturers' Motions For Summary Judgment Of Non-Infringement And Invalidity Depending On Claim Construction Dated July 10, 2008 from Rambus v. Hynix, et al., No. C-05-00334 RMW, Rambus v. Samsung, et al., C-05-02298 RMW and Rambus v. Micron, et al. No. C-06-00244 RMW	<input type="checkbox"/>	<input type="checkbox"/>
	32.	Clarified and Corrected Order on Rambus's Motion for Summary Judgment of Infringement, from Hynix Semiconductor Inc. et al. v. Rambus Inc., 00-20905 RMW (N.D. Cal.) dated January 19, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	33.	Cole, "Designers Try Alternatives to Asynchronous DRAMS: New Approach to Memory Needed," Electronic Engineering Times, Design, December 7, 1992	<input type="checkbox"/>	<input type="checkbox"/>
	34.	Colwell, Robert P. et. al. "Performance Effects of Architectural Complexity in the Intel 432", ACM Transactions on Computer Systems, Vol. 6, No. 3, pp. 296-339 (August 1988)	<input type="checkbox"/>	<input type="checkbox"/>
	35.	Combined Claims of EP 0 994 420 A2 und EP 1 197 830 A2 in the European Opposition Proceedings for EP 1 197 830 dated May 21, 2008	<input checked="" type="checkbox"/>	<input type="checkbox"/>
	36.	Comments by Third Party Requester Pursuant to 37 C.F.R. 1.947 dated May 15, 2008 In re Inter Partes Reexamination Control No. 95/001,008	<input type="checkbox"/>	<input type="checkbox"/>
	37.	Communication from European Patent Office for European Patent No. 1 022 642 dated May 9, 2005	<input type="checkbox"/>	<input type="checkbox"/>
	38.	Communication in Opposition Proceedings in EP 1 002 642 dated May 9, 2005	<input checked="" type="checkbox"/>	<input type="checkbox"/>
	39.	Communication of Notices of Opposition dated 4-21-2008 in European Patent No. 1,640 847	<input type="checkbox"/>	<input type="checkbox"/>

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			YES	NO
	40.	Comparison of Application No. EP 1 197 830 to WO91/16680 in the European Opposition Proceedings for EP 1 197 830 dated May 21, 2008	<input checked="" type="checkbox"/>	<input type="checkbox"/>
	41.	Consolidated Reply in Support of Rambus's Motions for Summary Judgment, dated November 16, 2007, from Rambus Inc. v. Hynix Semiconductor Inc. et al., No. 05-00334 RMW (N.D.Cal), Rambus Inc. v. Samsung Electronics Co., Ltd., et al. No. 05-002298 RMW (N.D. Cal.) and Rambus Inc. v. Micron Technology, Inc. et al., No. 06-00244 RMW (N.D. Cal.)	<input type="checkbox"/>	<input type="checkbox"/>
	42.	Decision Granting Petition to Waive 37 C.F.R. §1.98(a)(2) and Enter Into the File Documents from Co-Pending Reexamination Proceeding dated April 17, 2008 In re Inter Partes Reexamination Control No. 95/001,013	<input type="checkbox"/>	<input type="checkbox"/>
	43.	Decision Regarding Third Party Requester's Response to Order to Show Cause and Patent Owner's Comments Thereon mailed April 7, 2008 in <i>Inter Partes</i> Reexamination Control No. 95/000,166	<input type="checkbox"/>	<input type="checkbox"/>
	44.	Decision Regarding Third Party Requester's Response to Order to Show Cause and Patent Owner's Comments Thereon mailed April 7, 2008 in <i>Inter Partes</i> Reexamination Control No. 95/000,183	<input type="checkbox"/>	<input type="checkbox"/>
	45.	Decision Regarding Third Party Requester's Response to Order to Show Cause and Patent Owner's Comments Thereon mailed April 7, 2008 in <i>Inter Partes</i> Reexamination Control No. 95/000,250	<input type="checkbox"/>	<input type="checkbox"/>
	46.	Decision Revoking European Patent No. 1 004 956, including Summary of Facts and Submissions dated February 9, 2005	<input type="checkbox"/>	<input type="checkbox"/>
	47.	Declaration of David L. Taylor in Opposition to Rambus's Motion for Summary Judgment of Infringement from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.) dated February 10, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	48.	Declaration of David L. Taylor in Support of Hynix's Motions for Summary Judgment from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.) dated January 12, 2004.	<input type="checkbox"/>	<input type="checkbox"/>

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			YES	NO
	49.	Declaration of David L. Taylor in Support of Hynix's Motions for Summary Judgment of Non-Infringement and Invalidity dated March 4, 2005 from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.)	<input type="checkbox"/>	<input type="checkbox"/>
	50.	Declaration of David L. Taylor in Support of Hynix's Responsive Claim Construction Brief, from Hynix Semiconductor Inc. et al. v. Rambus Inc., No. 00-20905 RMW (N.D. Cal.) dated November 10, 2003.	<input type="checkbox"/>	<input type="checkbox"/>
	51.	Declaration of David L. Taylor in Support of Motion for Relief from the Court's January 19, 2005 Order Granting Summary Judgment of Infringement from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.) dated January 24, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	52.	Declaration of Joseph McAlexander in Support of Manufacturer's Joint Opposition to Rambus's Motions for Summary Judgment of Literal Infringement, dated October 31, 2007, from Rambus Inc. v. Hynix Semiconductor Inc. et al., No. 05-00334 RMW (N.D. Cal.), Rambus Inc. v. Samsung Electronics Co., Ltd., et al. No. 05-002298 RMW (N.D. Cal.) and Rambus Inc. v. Micron Technology, Inc. et al., No. 06-00244 RMW (N.D. Cal.)	<input type="checkbox"/>	<input type="checkbox"/>
	53.	Declaration of Joseph McAlexander in Support of Manufacturer's Responsive Brief on Claim Construction from Rambus Inc. v. Hynix Semiconductor Inc. et al., No. 05-00334 RMW (N.D. Cal.), Rambus Inc. v. Samsung Electronics Co., Ltd., et al. No. 05-002298 RMW (N.D. Cal.), and Rambus Inc. v. Micron Technology, Inc. et al., No. 06-00244 RMW (N.D. Cal.) dated September 14, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	54.	Declaration of Joseph McAlexander in Support of the Manufacturer's Farmwald and Ware Motions for Summary Judgment from Rambus Inc. v. Hynix Semiconductor Inc. et al., No. 05-00334 RMW (N.D. Cal.), Rambus Inc. v. Samsung Electronics Co., Ltd., et al. No. 05-002298 RMW (N.D. Cal.), and Rambus Inc. v. Micron Technology, Inc. et al., No. 06-00244 RMW (N.D. Cal.) dated October 9, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	55.	Declaration of Robert J. Murphy in Support of Consolidated Reply in Support of Rambus's Motions for Summary Judgment dated November 16, 2007, from Rambus Inc. v. Hynix Semiconductor Inc. et al., No. 05-00334 RMW (N.D. Cal.), Rambus Inc. v. Samsung Electronics Co., Ltd., et al. No. 05-002298 RMW (N.D. Cal.) and Rambus Inc. v. Micron Technology, Inc. et al., No. 06-00244 RMW (N.D. Cal.)	<input type="checkbox"/>	<input type="checkbox"/>

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			YES	NO
	56.	Declaration of Robert J. Murphy in Support of Plaintiff's Opening Markman brief from Rambus Inc. v. Hynix Semiconductor Inc. et al., No. 05-00334 RMW (N.D. Cal.), Rambus Inc. v. Samsung Electronics Co., Ltd., et al. No. 05-002298 RMW (N.D. Cal.), and Rambus Inc. v. Micron Technology, Inc. et al., No. 06-00244 RMW (N.D. Cal.) dated August 24, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	57.	Declaration of Robert J. Murphy in Support of Rambus' Opening Claim Construction Brief from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.) dated October 27, 2003.	<input type="checkbox"/>	<input type="checkbox"/>
	58.	Declaration of Robert J. Murphy in Support of Rambus's Motion for Summary Judgment of Infringement from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.) dated January 12, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	59.	Declaration of Robert J. Murphy in Support of Rambus's Motions for Summary Judgment of Infringement, dated October 5, 2007, from Rambus Inc. v. Hynix Semiconductor Inc. et al., No. 05-00334 RMW (N.D. Cal.), Rambus Inc. v. Samsung Electronics Co., Ltd., et al. No. 05-002298 RMW (N.D. Cal.) Rambus Inc. v. Micron Technology, Inc. et al., No. 06-00244 RMW (N.D. Cal.)	<input type="checkbox"/>	<input type="checkbox"/>
	60.	Declaration of Robert J. Murphy in Support of Rambus's Oppositions to Hynix's Motions for Summary Judgment from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.) dated February 11, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	61.	Declaration of Robert J. Murphy in Support of Rambus's Opposition to Manufacturer's Joint Motion for Summary Judgment, from Rambus Inc. v. Hynix Semiconductor Inc. et al., No. 05-00334 RMW (N.D. Cal.), Rambus Inc. v. Samsung Electronics Co., Ltd., et al. No. 05-002298 RMW (N.D. Cal.), and Rambus Inc. v. Micron Technology, Inc. et al., No. 06-00244 RMW (N.D. Cal.) dated November 2, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	62.	Defendant Infineon Technologies AG's First Supplemental Responses to Plaintiff's Interrogatories Nos. 3, 5, and 6, dated December 4, 2000, from RAMBUS Inc. V. Infineon Technologies AG et al., No. 3:00CV524 (E.D. Va)	<input type="checkbox"/>	<input type="checkbox"/>
	63.	Defendant Samsung's Supplemental Preliminary Invalidity Contentions dated April 28, 2008 in Rambus Inc. v. Samsung Electronics Co., Ltd, et al., No. C 00-02298 RMW (N.D. Cal.)	<input type="checkbox"/>	<input type="checkbox"/>
	64.	Designation of OI and OII Outline in the European Opposition Proceedings for EP 1 197 830 dated May 21, 2008	<input checked="" type="checkbox"/>	<input type="checkbox"/>

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			YES	NO
	65.	Diede, "The Titan Graphics Supercomputer Architecture," Computer, pp. 13-30, September 1988	<input type="checkbox"/>	<input type="checkbox"/>
	66.	Electric Engineering Times, A CMP Publication, Issue 724, December 7, 1992.	<input type="checkbox"/>	<input type="checkbox"/>
	67.	European Search Report from European Application No. 02 00 0376 dated August 10, 2005	<input type="checkbox"/>	<input type="checkbox"/>
	68.	European Search Report from European Application No. 99 11 8308 dated April 27, 2000	<input type="checkbox"/>	<input type="checkbox"/>
	69.	Examination Report from European Application No. 00 10 0018 dated October 16, 2003	<input type="checkbox"/>	<input type="checkbox"/>
	70.	Examination Report from European Application No. 00 10 8822 dated March 20, 2001	<input type="checkbox"/>	<input type="checkbox"/>
	71.	Examination Report from European Application No. 00 101 1832 dated August 7, 2000	<input type="checkbox"/>	<input type="checkbox"/>
	72.	Examination Report from European Application No. 02 00 0378 dated May 3, 2006	<input type="checkbox"/>	<input type="checkbox"/>
	73.	Examination Report from European Application No. 99 11 8308 dated April 3, 2002	<input type="checkbox"/>	<input type="checkbox"/>
	74.	Examination Report from European Application No. 99 11 8308 dated February 16, 2005	<input type="checkbox"/>	<input type="checkbox"/>
	75.	Examination Report, dated January 14, 1998, from European Application No. 91 98 374. 1	<input type="checkbox"/>	<input type="checkbox"/>

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			YES	NO
	76.	Examination Report, dated July 22, 1999, from European Application No 91 908 374.1	<input type="checkbox"/>	<input type="checkbox"/>
	77.	Exhibit A - Declaration of Joseph McAlexander in Support of Comments by Third Party Requester Pursuant to 37 C.F.R. 1.947 dated May 15, 2008 In re Inter Partes Reexamination Control No. 95/001,008	<input type="checkbox"/>	<input type="checkbox"/>
	78.	Exhibit B - File History of 5,319,755 in Support of Comments by Third Party Requester Pursuant to 37 C.F.R. 1.947 dated May 15, 2008 In re Inter Partes Reexamination Control No. 95/001,008	<input type="checkbox"/>	<input type="checkbox"/>
	79.	Exhibit E - Donner, Patent Prosecution, Practice and Procedures before the U.S. Patent and Trademark Office, copyright 1996, in Support of Comments by Third Party Requester Pursuant to 37 C.F.R. 1.947 dated May 15, 2008 In re Inter Partes Reexamination Control No. 95/001,008	<input type="checkbox"/>	<input type="checkbox"/>
	80.	Exhibit G - Deposition of Robert Murphy, Pages 1-249, dated May 7, 2008 in Support of Comments by Third Party Requester Pursuant to 37 C.F.R. 1.947 dated May 15, 2008 In re Inter Partes Reexamination Control No. 95/001,008	<input type="checkbox"/>	<input type="checkbox"/>
	81.	Exhibit H - Transcript of Proceedings Before the Honorable Ronald M. Whyte, U.S. District Judge in Hynix et al. v. Rambus and Rambus v. Hynix, et al. C-00-20905 dated March 28, 2008 in Support of Comments by Third Party Requester Pursuant to 37 C.F.R. 1.947 dated May 15, 2008 In re Inter Partes Reexamination Control No. 95/001,008	<input type="checkbox"/>	<input type="checkbox"/>
	82.	Exhibit K to Request for Reexamination - Claim Chart for Claims 1-24, 27, and 32-37 of the U.S. Patent No. 6,038,195 based on the JEDEC Standard, with additional reference to Grover, Lofgren, and Johnson, from Reexamination Control No, 90/010,082 dated December 19, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	83.	Exhibit K to Request for Reexamination - Claim Chart for Claims 1, 8, 18, 31, 32, and 43-45 of U.S. Patent No. 6,697,295 based on the JEDEC Standard, from Reexamination Control No. 95/001,013 dated October 11, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	84.	Exhibit K to Request for Reexamination - Claim Chart for Claims 1, 2, 5, 14, 38, and 41 of U.S. Patent No. 6,715,020 based on the JEDEC Standard, from Reexamination Control No. 95/001,008 dated October 1, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	85.	Exhibit L to Request for Reexamination - Claim Chart for claims 1-24, 27, and 32-37 of the U.S. Patent No. 6,038,195 based on Park, with additional reference to Grover, Lofgren, and Johnson, from Reexamination Control No. 90/010,082 dated December 19, 2007	<input type="checkbox"/>	<input type="checkbox"/>

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		<b>Art Unit</b>	<b>2827</b>
		<b>Examiner Name</b>	<b>Nguyen, Tan</b>
		<b>Confirmation No.</b>	<b>7213</b>
<b>Sheet</b>	<b>9 of 23</b>	<b>Attorney Docket Number</b>	<b>57941.000062</b>

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			YES	NO
	86.	Exhibit L to Request for Reexamination - Claim Chart for Claims 1, 8, 18, 31, 32, and 43-45 of U.S. Patent No. 6,697,295 based on Park, with additional references to the JEDEC Standard, Inagaki, Johnson, Lofgren, and Grover, from Reexamination Control No. 95/001,013 dated October 11, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	87.	Exhibit L to Request for Reexamination - Claim Chart for Claims 1, 2, 5, 14, 38, and 41 of U.S. Patent No. 6,715,020 based on Park, with additional reference to the JEDEC Standard and Inagaki, from Reexamination Control No. 95/001,008 dated October 1, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	88.	Exhibit M to Request for Reexamination - Claim Chart for Claims 1-51 of U.S. Patent No. 6,697,295 based on the iAPX Manual, with additional reference to Inagaki, Rau, Johnson, Lofgren, and Grover, from Reexamination Control No. 95/001,013 dated October 11, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	89.	Exhibit M to Request for Reexamination - Claim Chart for claims 1-24, 27, and 32-37 of the U.S. Patent No. 6,038,195 based on the iAPX Manual, with additional reference to iAPX Specification, Grover, Lofgren and Johnson, from Reexamination Control No. 90,010,082 dated December 19, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	90.	Exhibit M to Request for Reexamination - Claim Chart for Claims 1-2, 6, 9-11, 13-16, 19-20, and 25-26 of U.S. Patent No. 6,452,863 based on Park, from Reexamination Control No. 95/000,250 dated June 28, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	91.	Exhibit M to Request for Reexamination - Claim Chart for Claims 1-49 of U.S. Patent No. 6,715,020 based on the iAPX Manual, with additional reference to Inagaki, Lofgren, and Grover, from Reexamination Control No 95/001,008 dated October 1, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	92.	Exhibit N to Request for Reexamination - Claim Chart for Claims 1-51 of U.S. Patent No. 6,697,295 based on Budde, with additional reference to Inagaki, Rau, Johnson, Lofgren, and Grover, from Reexamination Control No. 95/001,013 dated October 11, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	93.	Exhibit N to Request for Reexamination - Claim Chart for claims 1-24, 27, and 32-27 of the U.S. Patent No. 6,028,195 based on Budde, with additional reference to Grover, Lofgren, Johnson, and Rau, from Reexamination Control No. 90/010,082 dated December 19, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	94.	Exhibit N to Request for Reexamination - Claim Chart for Claims 1-2, 6, 9-11, 13-16, 19-20, and 25-26 of U.S. Patent No. 6,452,863 based on the JEDEC Standard, from Reexamination Control No. 95/000,250 dated June 28, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	95.	Exhibit N to Request for Reexamination - Claim Chart for Claims 1-49 of U.S. Patent No. 6,715,020 based on Budde, with additional reference to Inagaki, Lofgren, and Grover, from Reexamination Control No. 95/001,008 dated October 1, 2007.	<input type="checkbox"/>	<input type="checkbox"/>

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		<b>Examiner Name</b>	<b>Nguyen, Tan</b>
		<b>Confirmation No.</b>	<b>7213</b>
<b>Sheet</b>	<b>10 of 23</b>	<b>Attorney Docket Number</b>	<b>57941.000062</b>

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			YES	NO
	96.	Exhibit O to Request for Reexamination - Claim Chart for Claims 1-35 of U.S. Patent No. 6,452,863 based on the iAPX Manual, with additional reference to Inagaki, Johnson, Lofgren, and Grover, from Reexamination Control No. 95/000,250 dated June 28, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	97.	Exhibit O to Request for Reexamination - File History of U.S. Patent No. 6,038,195, from Reexamination Control No. 90/010,082 dated December 19, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	98.	Exhibit O to Request for Reexamination - File History of U.S. Patent No. 6,715,020, from Reexamination Control No. 95/001,008 dated October 1, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	99.	Exhibit P to Request for Reexamination - Claim Chart for Claims 1-35 of U.S. Patent No. 6,452,863 based on Budde, with additional reference to Inagaki, Johnson, Lofgren, and Grover, from Reexamination Control No. 95/000,250 dated June 28, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	100.	Exhibit P to Request for Reexamination - File History of U.S. Patent No. 6,697,295, from Reexamination Control No. 95/001,013 dated October 11, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	101.	Exhibit P to Request for Reexamination - File History of U.S. Patent No. 5,841,580, from Reexamination Control No. 90/010,082 dated December 19, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	102.	Exhibit P to Request for Reexamination - File History of U.S. Patent No. 6,751,696, from Reexamination Control No. 95/001,008 dated October 1, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	103.	Exhibit Q to Request for Reexamination - Claim Chart for Claims 14-16 of U.S. Patent No. 6.4 52,863 based on Hynix-1 and Hynix.2, from Reexamination Control No. 95/000,250 dated June 28, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	104.	Exhibit Q to Request for Reexamination - File History of U.S. Patent No. 6,314,051, from Reexamination Control No. 95/001,013 dated October 11, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	105.	Exhibit S to Request for Reexamination - File History of U.S. Patent No. 6,034,918, from Reexamination Control No. 95/000.250 dated June 28, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	106.	Expert Report of Robert J. Murphy Regarding Claim Construction, dated August 17, 2001, from Micron Technology, Inc. v. Rambus Inc., No. 00-792 (RRM) (D. Del.)	<input type="checkbox"/>	<input type="checkbox"/>

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			YES	NO
	107.	Expert Report of Robert J. Murphy Regarding Patent Validity dated April 13, 2005, from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.)	<input type="checkbox"/>	<input type="checkbox"/>
	108.	Extended European Search Report from European Application No. 05 02 6720 dated April 13, 2006	<input type="checkbox"/>	<input type="checkbox"/>
	109.	Extended European Search Report from European Application No. 06 12 5946 dated August 24, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	110.	Extended European Search Report from European Application No. 06 12 5954.5 dated July 18, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	111.	Extended European Search Report from European Application No. 06 12 5958.6 dated August 6, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	112.	Extraction defining variations of the word "Operation" from "The new IEE Standard Dictionary of Electrical and Electronic Terms" - January 15, 1993 in the European Opposition Proceedings for EP 1 197 830 dated May 21, 2008	<input checked="" type="checkbox"/>	<input type="checkbox"/>
	113.	Extraction defining variations of the word "Operation" from Academic Press Dictionary of Science and Technology - 1992 in the European Opposition Proceedings for EP 1 197 830 dated May 21, 2008	<input checked="" type="checkbox"/>	<input type="checkbox"/>
	114.	Extraction defining variations of the word "Operation" from IEEE Standard Dictionary of Electrical and Electronic Terms - November 3, 1988 in the European Opposition Proceedings for EP 1 197 830 dated May 21, 2008	<input checked="" type="checkbox"/>	<input type="checkbox"/>
	115.	Extraction defining variations of the word "Operation" from Microsoft Press - Computer Dictionary - 1991 in the European Opposition Proceedings for EP 1 197 830 dated May 21, 2008	<input checked="" type="checkbox"/>	<input type="checkbox"/>
	116.	Hynix's Motion for Administrative Relief to File Hynix's Second Supplemental Brief in Support of its Motion for New Trial in Invalidity Due to Prior Art from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.) dated January 4, 2008.	<input type="checkbox"/>	<input type="checkbox"/>

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		<b>Confirmation No.</b>	<b>7213</b>
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			YES	NO
	117.	Hynix's Motion for Leave to File Motion for Partial Reconsideration of Claim Construction and Summary Judgment Orders from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.) dated August 5, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	118.	Hynix's Motion for Reconsideration of Construction of "Device" and Related Summary Judgment Orders from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.) dated October 18, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	119.	Hynix's Opposition to Rambus's Motion for Summary Judgment of Infringement by Hynix's Counterclaim Products, dated November 2, 2007, from Rambus Inc. v. Hynix Semiconductor Inc. et al. No. 05-00334 RMW (N.D. Cal)	<input type="checkbox"/>	<input type="checkbox"/>
	120.	Hynix's Reply in Support of its Motion for Reconsideration of Construction of "Device" and Related Summary Judgment Orders from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.) dated November 7, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	121.	Hynix's Reply Re: Motion for a New Trial on Invalidity Based on Prior Art from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.) dated June 13, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	122.	Hynix's Supplemental Brief in Support of its Motion for New Trial in Invalidity Due to Prior Art, from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.) dated July 6, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	123.	Intel 8203 64K Dynamic Ram Controller, pp 2:1-15 dated November 1997 cited in EP 1 197 830.	<input type="checkbox"/>	<input type="checkbox"/>
	124.	Interlocutory Decision in Opposition Proceedings, dated November 27, 2002, from European Patent No. EP 0 525 068.	<input type="checkbox"/>	<input type="checkbox"/>
	125.	JEDEC, "JC-42.3 Task Group Minutes - Meeting on Synchronous DRAM Ballots", (January 21, 1993)	<input type="checkbox"/>	<input type="checkbox"/>
	126.	JEDEC, "Minutes of Meeting No. 75 - JC-42.3 - Committee on RAM Memories", (May 24, 1995)	<input type="checkbox"/>	<input type="checkbox"/>

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		<b>Examiner Name</b>	<b>Nguyen, Tan</b>
		<b>Confirmation No.</b>	<b>7213</b>
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			YES	NO
	127.	Johnson, Dave et. al., "Intel iAPX 432-VLSI Building Blocks for a Fault -Tolerant Computer", AFIPS Conference Proceedings, 1983 National Computer Conference, pp. 531-537 (May 16-19, 1983)	<input type="checkbox"/>	<input type="checkbox"/>
	128.	Joint Claim Construction and Prehearing Statement Pursuant to Patent Local Rule 4-3 from Rambus Inc. v. Micron Technology inc. et al., No. 06-00244 RMW (N.D. Cal.) dated July 11, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	129.	KA660 CPU Module Technical Manual, ©Digital Equipment Corporation March 1990, cited in the European Opposition Proceedings for EP 1 197 830 dated May 21, 2008	<input type="checkbox"/>	<input type="checkbox"/>
	130.	Lines, Valerie et al., "High Speed Circuit Techniques in a 150Mhz 64M SDRAM", IEEE. pp. 8-11 (1997)	<input type="checkbox"/>	<input type="checkbox"/>
	131.	Manners, "Memory Survival Crisis," Electronics Weekly, Feature, April 1, 1992.	<input type="checkbox"/>	<input type="checkbox"/>
	132.	Manufacturer's Joint Motion for Summary Judgment of (1) Non-Infringement of the Farmwald Patents Under the Manufacturer's Proposed Claim Construction or (2) Invalidity of the Farmwald patents Under Rambus's Proposed claim Construction and Memorandum of Points and Authorities from Rambus Inc. v. Hynix Semiconductor Inc. et al., No. 05-00334 RMW (N.D.Cal), Rambus Inc. v. Samsung Electronics Co., Ltd., et al. No. 05-002298 RMW (N.D. Cal.), and Rambus Inc. v. Micron Technology, Inc. et al., No. 06-00244 RMW (N.D. Cal.) dated October 9 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	133.	Manufacturer's Joint Opposition to Rambus's Motions for Summary Judgment of Literal Infringement, dated November 2, 2007, from Rambus Inc. v. Hynix Semiconductor Inc. et al., No. 05-00334 RMW (N.D.Cal), Rambus Inc. v. Samsung Electronics Co., Ltd., et al. No. 05-002298 RMW (N.D. Cal.) and	<input type="checkbox"/>	<input type="checkbox"/>
	134.	Manufacturer's Joint Preliminary Invalidity Contentions from Rambus Inc. v. Hynix Semiconductor Inc. et al., No. 05-00334 RMW (N.D. Cal.) dated May 11, 2007.	<input type="checkbox"/>	<input type="checkbox"/>

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			YES	NO
	135.	Manufacturer's Joint Reply Brief in Support of the Manufacturer's Joint Motion for Summary Judgment of (1) Non-Infringement of the Farmland Patents Under the Manufacturer's Proposed Claim Construction or (2) of Non-Infringement of the Farmland/Horowitz Patents Under the Manufacturer's Proposed Claim Construction and Memorandum of Points and Authorities from Rambus Inc. v. Hynix Semiconductor Inc. et al., No. 05-00334 RMW (N.D.Cal), Rambus Inc. v. Samsung Electronics Co., Ltd., et al. No. 05-002298 RMW (N.D. Cal.), and Rambus Inc. v. Micron Technology, Inc. et al., No. 06-00244 RMW (N.D. Cal.) dated November 16, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	136.	Manufacturer's Supplemental Joint Preliminary Invalidity Contentions from Hynix v. Rambus, No. C 00-20905 RMW (N.D. Cal.), Rambus Inc. v. Hynix Semiconductor Inc. et al., No. 05-00334 RMW (N.D. Cal.) and Rambus Inc. v. Micron Technology, Inc. et al., No. 06-00244 RMW (N.D. Cal.) dated April 28, 2008.	<input type="checkbox"/>	<input type="checkbox"/>
	137.	Micron's Preliminary Invalidity Contentions from Rambus Inc. v. Micron Technology, Inc. et al., No. 06-00244 RMW (N.D.Cal.) dated May 11, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	138.	Minutes of Oral Proceedings from European Patent No. EP 1 004 956 dated January 24, 2008	<input type="checkbox"/>	<input type="checkbox"/>
	139.	MoSys, "MoSys Data Sheet - "MD904 to MD920, ½ to 2½ Mbyte Multibank DRAM (MDRAM) 128Kx32 to 656Kx32," Preliminary Information, pp. 1-16 (February 21, 1997)	<input type="checkbox"/>	<input type="checkbox"/>
	140.	MoSys, "MoSys Data Sheet - "MD904 to MD920, ½ to 2½ Mbyte Multibank DRAM (MDRAM) 128Kx32 to 656Kx32," Preliminary Information, pp. 1-14 (December 18, 1995)	<input type="checkbox"/>	<input type="checkbox"/>
	141.	MoSys, "MoSys Incorporated Technology White Paper", pp. 1-15 (July 1994)	<input type="checkbox"/>	<input type="checkbox"/>
	142.	Notice of Opposition to European Patent No 0 994 420 of Micron Semiconductor (Deutschland) GmbH, dated October 2, 2006	<input type="checkbox"/>	<input type="checkbox"/>
	143.	Notice of Opposition to European Patent No. 0 994 420 of Hynix Semiconductor Deutschland GmbH dated October 2, 2006 (with English-language translation)	<input type="checkbox"/>	<input checked="" type="checkbox"/>

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			YES	NO
	144.	Notice of Opposition to European Patent No. 0994 420 of Micron Europe Ltd. dated October 3, 2006	<input type="checkbox"/>	<input type="checkbox"/>
	145.	Notice of Opposition to European Patent No. 1 004 956 of Micron Europe Ltd. dated October 3, 2001	<input type="checkbox"/>	<input type="checkbox"/>
	146.	Notice of Opposition to European Patent No. 1 022 641 of Micron Europe Ltd. dated December 7, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	147.	Notice of Opposition to European Patent No. 1 022 642 of Micron Europe Ltd. dated September 10, 2001	<input type="checkbox"/>	<input type="checkbox"/>
	148.	Notice of Opposition to European Patent No. 1 197 830 of Hynix Semiconductor Deutschland GmbH, dated July 3, 2007 (with English-language translation)	<input type="checkbox"/>	<input checked="" type="checkbox"/>
	149.	Notice of Opposition to European Patent No. 1 197 830 of Micron Europe Ltd., dated July 3, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	150.	Notice of Opposition to European Patent No. EP 0 525 068, of Hyndai Electronics Deutschland GmbH, dated December 15, 2000 (with English-language translation)	<input type="checkbox"/>	<input checked="" type="checkbox"/>
	151.	Notice of Opposition to European Patent No. EP 0 525 068, of Infineon Technologies AG, dated November 23, 2000 (with English-language translation)	<input type="checkbox"/>	<input type="checkbox"/>
	152.	Notice of Opposition to European Patent No. EP 0 525 068, of Micron Europe Ltd., dated October 26, 2000	<input type="checkbox"/>	<input type="checkbox"/>
	153.	Notice of Prior and Concurrent Proceedings dated March 7, 2008 in Ex Part Reexamination Control No. 90/010,082	<input type="checkbox"/>	<input type="checkbox"/>
	154.	Notice of Prior and Concurrent Proceedings dated March 7, 2008 in Ex Part Reexamination Control No. 95/001,026	<input type="checkbox"/>	<input type="checkbox"/>

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>		<b>Application Number</b>	<b>10/716,595</b>
		<b>Filing Date</b>	<b>November 20, 2003</b>
		<b>First Named Inventor</b>	<b>Michael FARMWALD, et al.</b>
		<b>Art Unit</b>	<b>2827</b>
		<b>Examiner Name</b>	<b>Nguyen, Tan</b>
		<b>Confirmation No.</b>	<b>7213</b>
<b>Sheet</b>	<b>16 of 23</b>	<b>Attorney Docket Number</b>	<b>57941.000062</b>

<b>OTHER DOCUMENTS - NON-PATENT LITERATURE DOCUMENTS</b>				
*Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	TRANSLATION	
			YES	NO
	155.	Opinion of the Commission on Remedy, from In the Matter of Rambus, Inc., Docket No. 9302 (FTC) from Oral Hearing dated November 16, 2006	<input type="checkbox"/>	<input type="checkbox"/>
	156.	Opinion of the Commission, from In the Matter of Rambus Inc., Docket No. 9302 (FTC) dated July 31, 2006	<input type="checkbox"/>	<input type="checkbox"/>
	157.	Order Denying Hynix's Motion for Partial Reconsideration of the Claim Construction and Related Summary Judgment Orders from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.) dated February 21, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	158.	Order Denying Rambus's Motion for Summary Judgment of Infringement Relating to Access Time Register Limitations dated March 13, 2006 from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.)	<input type="checkbox"/>	<input type="checkbox"/>
	159.	Order Denying Rambus's Motion for Summary Judgment of Infringement Relating to Delay Locked Loop, dated February 23, 2006 from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.)	<input type="checkbox"/>	<input type="checkbox"/>
	160.	Order Denying Rambus's Motion for Summary Judgment of Infringement Relating to the In Response to Rising/Falling Claim Limitation dated March 10, 2005, from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.)	<input type="checkbox"/>	<input type="checkbox"/>
	161.	Order Granting Leave to File Motion for Partial Reconsideration of Claim Construction and Summary Judgment Orders from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.) dated October 3, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	162.	Order Granting Reexamination, mailed on January 9, 2008, from Reexamination Control No. 95/001,013	<input type="checkbox"/>	<input type="checkbox"/>
	163.	Order Granting Reexamination, mailed on October 19, 2007, from Reexamination Control No. 95/000,183	<input type="checkbox"/>	<input type="checkbox"/>
	164.	Order Granting Reexamination, mailed on October 19, 2007, from Reexamination Control No. 95/000,166	<input type="checkbox"/>	<input type="checkbox"/>

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		<b>Art Unit</b>	<b>2827</b>
		<b>Examiner Name</b>	<b>Nguyen, Tan</b>
		<b>Confirmation No.</b>	<b>7213</b>
<b>Sheet</b>	<b>17 of 23</b>	<b>Attorney Docket Number</b>	<b>57941.000062</b>

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			YES	NO
	165.	Order Granting Reexamination, mailed on October 19, 2007, from Reexamination Control No. 95/000,178	<input type="checkbox"/>	<input type="checkbox"/>
	166.	Order Granting Reexamination, mailed on September 5, 2007, from Reexamination Control No. 95/000,250	<input type="checkbox"/>	<input type="checkbox"/>
	167.	Order Granting Relief from Court's January 19, 2005 Order Granting Summary Judgment of Infringement from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.) dated March 4, 2004,	<input type="checkbox"/>	<input type="checkbox"/>
	168.	Order Granting Request for Inter Partes Reexamination mailed April 9, 2008 in Reexamination Control No. 95/001,026	<input type="checkbox"/>	<input type="checkbox"/>
	169.	Order on Rambus's Motion for Summary Judgment of Infringement, , from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.) dated January 5, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	170.	Order to Show Cause mailed February 22, 2008 in <i>Inter Partes</i> Reexamination Control No. 95/000,178.	<input type="checkbox"/>	<input type="checkbox"/>
	171.	Order to Show Cause mailed February 22, 2008 in <i>Inter Partes</i> Reexamination Control No. 95/000,166.	<input type="checkbox"/>	<input type="checkbox"/>
	172.	Order to Show Cause mailed February 22, 2008 in <i>Inter Partes</i> Reexamination Control No. 95/000,183.	<input type="checkbox"/>	<input type="checkbox"/>
	173.	Order to Show Cause mailed February 22, 2008 in <i>Inter Partes</i> Reexamination Control No. 95/000,250.	<input type="checkbox"/>	<input type="checkbox"/>
	174.	Overview of Claim 1 (Chart 2) for EP 0 994 420, EP 1 197 830, EP 1 022 641 and EP 1 640,847 in European Opposition Proceedings for EP 1 197 830 dated May 21, 2008.	<input checked="" type="checkbox"/>	<input type="checkbox"/>
	175.	Patent Owner's Comments on Third-Party Requester's Response to Orders to Show Cause dated March 19, 2008 in Inter Partes Reexamination Control Nos. 95/000,166 (6,426,916), 95/000,178 (6,324,120), 95/000,183 (6,182,184), and 95/000,250 (6,452,863)	<input type="checkbox"/>	<input type="checkbox"/>

<b>EXAMINER SIGNATURE</b>	<b>DATE CONSIDERED</b>
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		<b>Filing Date</b>	<b>November 20, 2003</b>
		<b>First Named Inventor</b>	<b>Michael FARMWALD, et al.</b>
		<b>Art Unit</b>	<b>2827</b>
		<b>Examiner Name</b>	<b>Nguyen, Tan</b>
		<b>Confirmation No.</b>	<b>7213</b>
<b>Sheet</b>	<b>18 of 23</b>	<b>Attorney Docket Number</b>	<b>57941.000062</b>

<b>OTHER DOCUMENTS - NON-PATENT LITERATURE DOCUMENTS</b>				
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			YES	NO
	176.	Pelgrom, et al., "A 32-kbit Variable-Length Shift Register for Digital Audio Application", ESSCIRC '86 Digital Tech Papers, pgs. 38-40.	<input type="checkbox"/>	<input type="checkbox"/>
	177.	Peterson, et al., "Two Chips Endow 32-Bit Processor with Fault-Tolerant Architecture, International Electronics, pgs. 159-164, April 7, 1983.	<input type="checkbox"/>	<input type="checkbox"/>
	178.	Petition to Vacate Filing Dates, Orders Granting Inter Partes Reexamination, and Office Action dated March 7, 2008 in Inter Partes Reexamination Control Nos. 95/001,008 (6,715,020), 95/001,013 (6,697,295) and 95/001,026( 6,378,020)	<input type="checkbox"/>	<input type="checkbox"/>
	179.	Petition to Waive 37 C.F.R. §1.98(a)(2) and Enter Into the File Documents from Co-Pending Reexamination Proceeding dated May 13, 2008 In re Inter Partes Reexamination Control No. 90/010,082	<input type="checkbox"/>	<input type="checkbox"/>
	180.	Petition to Waive 37 C.F.R. §1.98(a)(2) and Enter Into the File Documents from Co-Pending Reexamination Proceeding dated May 13, 2008 In re Inter Partes Reexamination Control No. 95/000,183	<input type="checkbox"/>	<input type="checkbox"/>
	181.	Petition to Waive 37 C.F.R. §1.98(a)(2) and Enter Into the File Documents from Co-Pending Reexamination Proceeding dated May 13, 2008 In re Inter Partes Reexamination Control No. 95/000,178	<input type="checkbox"/>	<input type="checkbox"/>
	182.	Petition to Waive 37 C.F.R. §1.98(a)(2) and Enter Into the File Documents from Co-Pending Reexamination Proceeding dated May 13, 2008 In re Inter Partes Reexamination Control No. 95/001,026	<input type="checkbox"/>	<input type="checkbox"/>
	183.	Petition to Waive 37 C.F.R. §1.98(a)(2) and Enter Into the File Documents from Co-Pending Reexamination Proceeding dated May 13, 2008 In re Inter Partes Reexamination Control No. 95/000,166	<input type="checkbox"/>	<input type="checkbox"/>
	184.	Petition to Waive 37 C.F.R. §1.98(a)(2) and Enter Into the File Documents from Co-Pending Reexamination Proceeding dated March 7, 2008 In re Inter Partes Reexamination Control No. 95/001,013	<input type="checkbox"/>	<input type="checkbox"/>

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		<b>First Named Inventor</b>	<b>Michael FARMWALD, et al.</b>
		<b>Art Unit</b>	<b>2827</b>
		<b>Examiner Name</b>	<b>Nguyen, Tan</b>
		<b>Confirmation No.</b>	<b>7213</b>
<b>Sheet</b>	<b>19 of 23</b>	<b>Attorney Docket Number</b>	<b>57941.000062</b>

<b>OTHER DOCUMENTS - NON-PATENT LITERATURE DOCUMENTS</b>				
*Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	TRANSLATION	
			YES	NO
	185.	Plaintiff's Notice of Motion and Motion for Summary Judgment of Infringement by Samsung's Counterclaim Products; Memorandum of Points and Authorities in Support Thereof, from Rambus Inc. v. Hynix Semiconductor Inc. et al., No. 05-00334 RMW (N.D.Cal), Rambus Inc. v. Samsung Electronics Co., Ltd., et al. No. 05-002298 RMW (N.D. Cal.) dated October 5, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	186.	Plaintiff's Notice of Motion and Motion for Summary Judgment of Infringement by Nanya's Counterclaim Products; Memorandum of Points and Authorities in Support Thereof, , from Rambus Inc. v. Hynix Semiconductor Inc. et al., No. 05-00334 RMW (N.D.Cal) dated October 5, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	187.	Plaintiff's Notice of Motion and Motion for Summary Judgment of Infringement by Nanya's DDR2 SDRAM Devices; Memorandum of Points and Authorities in Support Thereof, from Rambus Inc. v. Hynix Semiconductor Inc. et al. No. 05-00334 RMW (N.D. Cal) dated October 5, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	188.	Plaintiff's Notice of Motion and Motion for Summary Judgment of Infringement by Hynix's DDR2, gDDR2, and GDDR3 Devices; Memorandum of Points and Authorities in Support Thereof, from Rambus Inc. v. Hynix Semiconductor Inc. et al. No. 05-00334 RMW (N.D. Cal) dated October 5, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	189.	Plaintiff's Notice of Motion and Motion for Summary Judgment of Infringement by Samsung's DDR2, gDDR2 and GDDR3 Products; Memorandum of Points and Authorities in Support Thereof, from Rambus Inc. v. Hynix Semiconductor Inc. et al. No. 05-00334 RMW (N.D. Cal) dated October 5, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	190.	Plaintiff's Notice of Motion and Motion for Summary Judgment of Infringement by Samsung's SDRAM and DDR SDRAM Devices; Memorandum of Points and Authorities in Support Thereof, from Rambus Inc v. Samsung Electronics Co., Ltd. et al. No. 05-002298 RMW (N.D.Cal.) dated October 5, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	191.	Plaintiff's Notice of Motion and Motion for Summary Judgment of Infringement by Micron's Counterclaim Products; Memorandum of Points and Authorities in Support Thereof dated, October 5, 2007 from Rambus Inc. v. Micron Technology, Inc. et al., No. 06-00244 RMW (N.D. Cal.)	<input type="checkbox"/>	<input type="checkbox"/>

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		<b>Filing Date</b>	<b>November 20, 2003</b>
		<b>First Named Inventor</b>	<b>Michael FARMWALD, et al.</b>
		<b>Art Unit</b>	<b>2827</b>
		<b>Examiner Name</b>	<b>Nguyen, Tan</b>
		<b>Confirmation No.</b>	<b>7213</b>
<b>Sheet</b>	<b>20 of 23</b>	<b>Attorney Docket Number</b>	<b>57941.000062</b>

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			YES	NO
	192.	Plaintiff's Notice of Motion and Motion for Summary Judgment of Infringement by Micron's DDR2, GDDR3, and RLDRAM II Devices; Memorandum of Points and Authorities in Support Thereof, dated October 5, 2007, from Rambus Inc. v. Micron Technology, Inc., at al., No. 06-0244 RMW (N.D. Cal)	<input type="checkbox"/>	<input type="checkbox"/>
	193.	Plaintiff's Notice of Motion and Motion for Summary Judgment of Infringement by Hynix's Counterclaim Products; Memorandum of Points and Authorities in Support Thereof, dated October 5, 2007, from Rambus Inc. v. Hynix Semiconductor Inc. et al. No. 05-00334 RMW (N.D. Cal)	<input type="checkbox"/>	<input type="checkbox"/>
	194.	Quinnell, "Coupling DRAM and Bus Technology Yields 500-MHz Burst Transfers," EDN-Electronic Technology for Engineers and Engineering Managers Worldwide, a Cahners Publication, pp. 77-78, March 30, 1992	<input type="checkbox"/>	<input type="checkbox"/>
	195.	Quinnell, "Synchronous Memories," EDN-Electronic Technology for Engineers and Engineering Managers Worldwide, pp. 56-66, August 4, 1994	<input type="checkbox"/>	<input type="checkbox"/>
	196.	Rambus Inc.'s Opposition to Hynix's Motion for Administrative Relief to File Hynix's Second Supplemental Brief in Support of its Motion for New Trial in Invalidity Due to Prior Art from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.) dated January 9, 2008.	<input type="checkbox"/>	<input type="checkbox"/>
	197.	Rambus Inc's Disclosure of Asserted Claims and Preliminary Infringement Contentions Pursuant to Patent Local Rules from Rambus Inc. v. Samsung Electronics Co., Ltd. et al., No. 05-002298 RMW (N.D. Cal) dated April 3, 2006	<input type="checkbox"/>	<input type="checkbox"/>
	198.	Rambus's Notice of Motion and Motion for Summary Judgment of Infringement; <u>Corrected</u> Memorandum of Points and Authorities dated from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.) January 27, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	199.	Rambus's Opposition to Hynix's Motion for Reconsideration of the Court's Claim Construction Ruling on "Device" and the Court's Summary Judgment Order's from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.) dated October 31, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	200.	Rambus's Opposition to Hynix Semiconductor Inc.'s Supplemental Brief in Support of its Motion for New Trial in Invalidity Due to Prior Art from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.) dated August 3, 2007.	<input type="checkbox"/>	<input type="checkbox"/>

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			YES	NO
	201.	Rambus's Opposition to Manufacturer's Joint Motion for Summary Judgment of (1) Invalidity of Farmwald/Horowitz Patents-In-Suit Under Rambus's Proposed Claim Construction or (2) of Non-Infringement of the Farmland/Horowitz Patents Under the Manufacturer's Proposed Claim Construction and Memorandum of Points and Authorities from Rambus Inc. v. Hynix Semiconductor Inc. et al., No. 05-00334 RMW (N.D.Cal), Rambus Inc. v. Samsung Electronics Co., Ltd., et al. No. 05-002298 RMW (N.D. Cal.), and Rambus Inc. v. Micron Technology, Inc. et al., No. 06-00244 RMW (N.D. Cal.) dated November 2, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	202.	Rambus's Reply Regarding Summary Judgment of Infringement by Hynix's Counterclaim Products, dated November 16, 2007, from Rambus Inc. v. Hynix Semiconductor Inc. et al. No. 05-00334 RMW (N.D. Cal)	<input type="checkbox"/>	<input type="checkbox"/>
	203.	Remedy Statement of Commissioner Pamela Jones Harbour Concurring in Part and Dissenting in Part, from In the Matter of Rambus, Inc. Docket No. 9302 (FTC) from Oral Hearing dated November 15, 2006	<input type="checkbox"/>	<input type="checkbox"/>
	204.	Reply Declaration of Robert J. Murphy in Support of Rambus's Motion for Summary Judgment from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.) dated March 3, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	205.	Reply Declaration of Robert J. Murphy in Support of Rambus's Reply Claim Construction Brief from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.) dated November 17, 2003.	<input type="checkbox"/>	<input type="checkbox"/>
	206.	Reply Declaration of Robert J. Murphy in Support of Reply to Manufacturer's Responsive Brief on Claim Construction from Rambus Inc. v. Hynix Semiconductor Inc. et al., No. 05-00334 RMW (N.D.Cal), Rambus Inc. v. Samsung Electronics Co., Ltd., et al. No. 05-002298 RMW (N.D. Cal.), and Rambus Inc. v. Micron Technology, Inc. et al., No. 06-00244 RMW (N.D. Cal.) dated September 28, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	207.	Request for Reexamination of U.S. Patent No. 6,715,020 dated October 1,2007, from Reexamination Control No. 95/001,008	<input type="checkbox"/>	<input type="checkbox"/>
	208.	Request for Reexamination of U.S. Patent No. 6,038,195 dated December 19, 2007, from Reexamination Control No. 90/010,082	<input type="checkbox"/>	<input type="checkbox"/>
	209.	Request for Reexamination of U.S. patent No, 6,378,020 dated January 28, 2008, from Reexamination Control No. 95/002,026	<input type="checkbox"/>	<input type="checkbox"/>

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		<b>Filing Date</b>	<b>November 20, 2003</b>
		<b>First Named Inventor</b>	<b>Michael FARMWALD, et al.</b>
		<b>Art Unit</b>	<b>2827</b>
		<b>Examiner Name</b>	<b>Nguyen, Tan</b>
		<b>Confirmation No.</b>	<b>7213</b>
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			YES	NO
	210.	Siemens AG, "Siemens ICs for Entertainment Electronics Picture-in-Picture System, Siemens Edition, August 1989.	<input type="checkbox"/>	<input type="checkbox"/>
	211.	SLDRAM, "SLDRAM Architectural and Functional Overview," pgs. 1-18 as filed in Application Serial No. 60/057,092 filed August 27, 1997.	<input type="checkbox"/>	<input type="checkbox"/>
	212.	Summons to Attend Oral Proceedings Pursuant to Rule 71(1) EPC, dated May 27, 2002, from European Patent No. 0 525 068	<input type="checkbox"/>	<input type="checkbox"/>
	213.	Supplemental Declaration of David L. Taylor in Support of Hynix's Motions for Summary Judgment from Hynix Semiconductor Inc. et al. v. Rambus Inc. No. 00-20905 RMW (N.D. Cal.) dated March 3, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	214.	Supplemental Declaration of Joseph McAlexander in Support of the Manufacturer's Joint Reply Regarding the Manufacturer's Farmwald and Ware Motions for Summary Judgment, from Rambus Inc. v. Hynix Semiconductor Inc. et al., No. 05-00334 RMW (N.D. Cal.), Rambus Inc. v. Samsung Electronics Co., Ltd., et al. No. 05-002298 RMW (N.D. Cal.), and Rambus Inc. v. Micron Technology, Inc. et al., No. 06-00244 RMW (N.D. Cal.) dated November 16, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	215.	Third-Party Requester's Petition for Clarification of Real Party In Interest and Opposition to Petition to Vacate Filing Dates, Orders Granting Inter Partes Reexamination and, Office Actions dated March 20, 2008 in Inter Partes Reexamination Control Nos. 95/001,008 (6,715,020), 95/001,013 (6,697,295) and 95/001,026( 6,378,020)	<input type="checkbox"/>	<input type="checkbox"/>
	216.	Third-Party Requester's Response to Orders to Show Cause dated March 4, 2008 in Inter Partes Reexamination Control Nos. 95/000,166 (6,426,916), 95/000,178 (6,324,120), 95/000,183 (6,182,184), and 95/000,250 (6,452,863)	<input type="checkbox"/>	<input type="checkbox"/>
	217.	Tuite, "Cache Architectures Under Pressure to Match CPU Performance," Computer Design for Electronic Engineers and Engineering Managers," pp. 91-97, March 1993.	<input type="checkbox"/>	<input type="checkbox"/>
	218.	Wilson, "A Cure for the Bottleneck Blues," Computer Design for Electronic Engineers and Engineering Managers," pp. 38 & 40, May 19, 1992.	<input type="checkbox"/>	<input type="checkbox"/>

<b>EXAMINER SIGNATURE</b>	<b>DATE CONSIDERED</b>
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>		<b>Application Number</b>	<b>10/716,595</b>
		<b>Filing Date</b>	<b>November 20, 2003</b>
		<b>First Named Inventor</b>	<b>Michael FARMWALD, et al.</b>
		<b>Art Unit</b>	<b>2827</b>
		<b>Examiner Name</b>	<b>Nguyen, Tan</b>
		<b>Confirmation No.</b>	<b>7213</b>
<b>Sheet</b>	<b>23 of 23</b>	<b>Attorney Docket Number</b>	<b>57941.000062</b>

<b>OTHER DOCUMENTS - NON-PATENT LITERATURE DOCUMENTS</b>				
*Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	TRANSLATION	
			YES	NO
	219.	Wilson, MoSys Details High-Speed Graphics DRAM, Electronics Engineering Times, July 18, 2994	<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>
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			<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>

<b>EXAMINER SIGNATURE</b>	<b>DATE CONSIDERED</b>
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